

Resource and Performance Tradeoffs

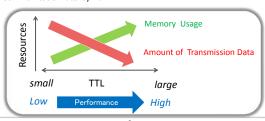
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TTL Value: Large

- ■Memory Usage: <u>Large</u>
- Amount of Transmission Data: Small

Performance: High

- Cache hit ratio: High
- Communication latency: Low



Issue and Approach

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- Issue
 - ■To evaluate the impact of TTL-based caching on network resources and performance in the caching hierarchies
- Approach
 - ■To model the cache characteristics of the hierarchical cache system using a request propagation matrix for each caching hierarchy

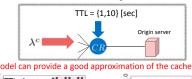
Model of Cache Hit Ratio of a CR

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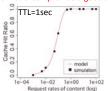
■ Cache Hit Ratio using TTL at a Cache Node [8]

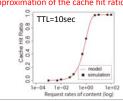
$$f(\lambda^c, TTL^c) = 1 - e^{-\lambda^c TTL^c}$$

 $\lambda^c \colon$ Request rate of content c based on an exponential distributed interval



This model can provide a good approximation of the cache hit ratio.





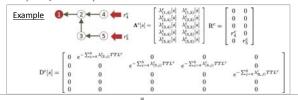
Request Propagation Model

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■ Request Propagation Model in a Caching Hierarchy

$$\begin{split} & \boldsymbol{\Lambda}^{c}[s+1] = \mathbf{D}^{c}[s] \cdot \boldsymbol{\Lambda}^{c}[s] + \mathbf{R}^{c}, \forall c \\ & \boldsymbol{\Lambda}^{c}[s] \coloneqq \begin{bmatrix} \boldsymbol{\lambda}^{c}_{(1,1)[s]} & \cdots & \boldsymbol{\lambda}^{c}_{(1,N)[s]} \\ \vdots & \ddots & \vdots \\ \boldsymbol{\lambda}^{c}_{(M,1)[s]}[s] & \cdots & \boldsymbol{\lambda}^{c}_{(M,N)[s]} \end{bmatrix} | \mathbf{R}^{c}|_{jj} \coloneqq \left\{ \begin{array}{c} r^{c}_{j}, \text{when } CR_{t} \text{ is located on } j\text{-th request site} \\ 0 & \text{otherwise} \end{array} \right. \\ & \mathbf{D}^{c}[s] \coloneqq \left\{ \begin{array}{c} 1 - f(\sum_{j}^{c}) \boldsymbol{\lambda}^{c}_{(M,N)[s]}(s], TTL^{c}), \forall m = parent.node(n) \leftarrow \mathsf{Cache \ Miss \ Ratio \ of \ } CRn \end{array} \right. \end{split}$$

s: The number of steps that each request propagates to the next CR $\lambda_{i,j}^c$: Request rate of content c from the requesting user in site j to CR_i r_i^c : Request rates r_i^c of content c from users in site i



Model of Resources / Performance

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■ Memory usage of content c at CRi

$$U_i^c := \theta_c f(\sum \lambda_{(i,j)}^c, TTL^c).$$
 θ_c : Data size

Total amount of data delivery of content c through all CRS

$$\begin{array}{ll} \operatorname{Dt}^c := \theta_c \sum_j Tr^c_j & \operatorname{Hop-length} \\ \operatorname{between two } \mathit{CRs} \\ \operatorname{Tr}^c & := & [Tr^c_1 \cdots Tr^c_j \cdots Tr^c_N]^{\operatorname{T}} = (\underbrace{\mathbf{H}} * \mathbf{\Lambda}^c)^{\operatorname{T}} \\ & \vdots \\ f(\sum_i \lambda^c_{(M,i)}, TTL^c) \end{array}$$

■ Cache hit ratio of content c in the network

CHR^c :=
$$1 - \frac{\sum_{j}^{N} \lambda_{(o,j)}^{c} \left(1 - f(\sum_{j}^{N} \lambda_{(o,j),TTL^{c}}^{c})\right)}{\sum_{j}^{M} r^{c}}$$
.

Average Hop Length of content c

$$\mathbf{AHL}^c := \frac{\sum_{j}^{N} \left(Tr_j^c + \mathbf{Hp}^o \lambda_{(o,j)}^c (1 - \mathbf{f}(\sum_{k}^{N} \lambda_{(o,k)}^c, TTL^c) \right)}{\sum_{j}^{N} r_j^c}$$

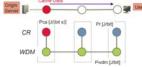
Model of Power Consumption

■ Cache Allocation Power: power consumption [J] for storing data of content c in 1 sec

$$\mathbf{CP}^c := \theta_c P_{ca} \sum_{i}^{M} f(\sum_{j}^{N} \lambda_{(i,j)}^c, TTL^c),$$

■ Traffic Transmission Power: power consumption [J] for delivering content c

$$TP^c := (P_r + P_{wdm})Dt^c$$



Assumption

- Energy Proportional Networks which power consumption of each device is proportional to its usage for a network composed of *CR*s and Wavelength Division Multiplexing (WDM)

Evaluation1: Model Verification

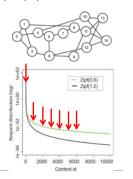
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Approach

■ To compare "cache hit rate" and "average hop length" for 7 contents measured by simulation with those estimated by the proposed model

Evaluation Conditions

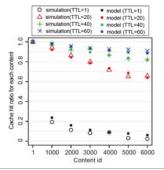
- ▶ Target network: NSF network
- TTL value :{1,20,40,60} [sec]
- ▶ 7 content items in 10000 content items:
 - {1,1000,2000,...,6000}
- Origin site: Uniform distribution
- Content size: Geometric distributionAverage size 10 Mbytes
- ▶ The total number of requests from each site:
 - 100 [requests/sec]
- ► Content popularity: Zipf(0.8)
- ▶ Memory size of each CR: sufficiently large
- ▶ Simulation time: 3200 [sec]

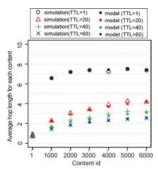


Verification Results

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The cache hit ratio and average hop length for each content provide suitable approximations of the simulation results.





Evaluation2: Impact of TTL

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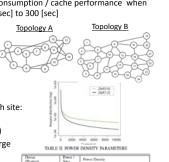
Approach

■ To evaluate resource usage / power consumption / cache performance when TTL of all content is changed from 1 [sec] to 300 [sec]

Evaluation Conditions

- ▶ Target network: 2 Topologies
- The number of content Items: 10000
- ▶ Origin site: Uniform distribution
- Content size: Geometric distribution
- Average size 10 MbytesThe total number of requests from each site:
- ▶ 100 [requests/sec]
- ▶ Content popularity: Zipf(0.8), Zipf(1.2)
- Memory size of each CR: sufficiently large
- ▶ Power density parameters:Table2





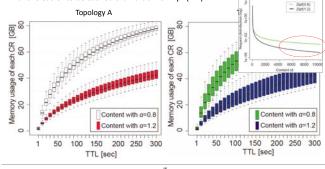


Memory Usage of Each CR



■ The memory usage of each CR becomes larger as the TTL value becomes larger.

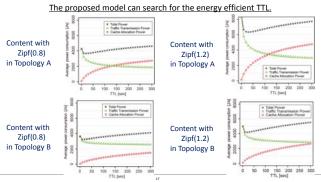
■ The memory usage for content with Zipf (0.8) is larger than that for content with Zipf (1.2) because less popular content with Zipf (0.8) has higher request rates and is easier to be cached than that with Zipf (1.2).



Power Consumption

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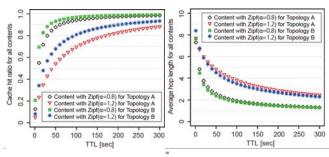
- These data demonstrate the tradeoff between "cache allocation power" and "traffic transmission power" for the change of the TTL value.
- The energy impact of TTL is also different depending on the network conditions.



Cache Performance

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- The cache hit ratio approaches around 100 % as the average hop length is approaching 1 and the memory usage becomes larger.
 - The average hop length of 1 hop means that all content items are cached.
- The average hop length of all content items becomes smaller as the TTL becomes larger.



Conclusions

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Summary

■ Analytical Model using TTL

- Evaluate the cache characteristics in the distributed cache system
- Provide a design guideline for TTL of content in view of energy efficiency or efficient memory usage

■ Future Work

- ■We will study the following mechanisms using the proposed model.
 - "Memory Control Mechanism
 - "Cache Probability Control Mechanism"

19